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		PDM Clock J3 IN > OUT 33 GND (UREF level max.) StATIE 6TRE 6TRE			2
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Α	В	C	Title: Assembly Drawing Board: =Board_name Project: =ProjectTitle Author: =Designer D D	Version: 1.0.0 Size: A4 Sheet: 1 of 2 Date: 25/08/2021	4 CATIE CC BY 4.0

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